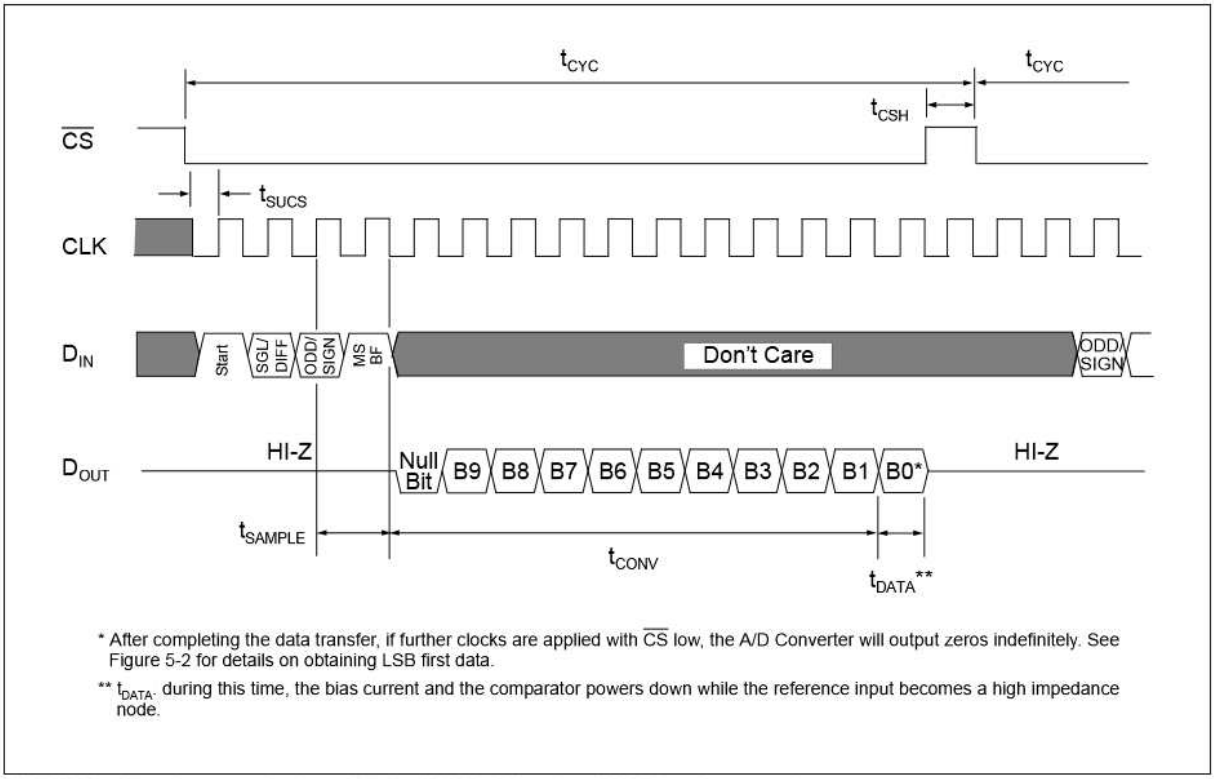
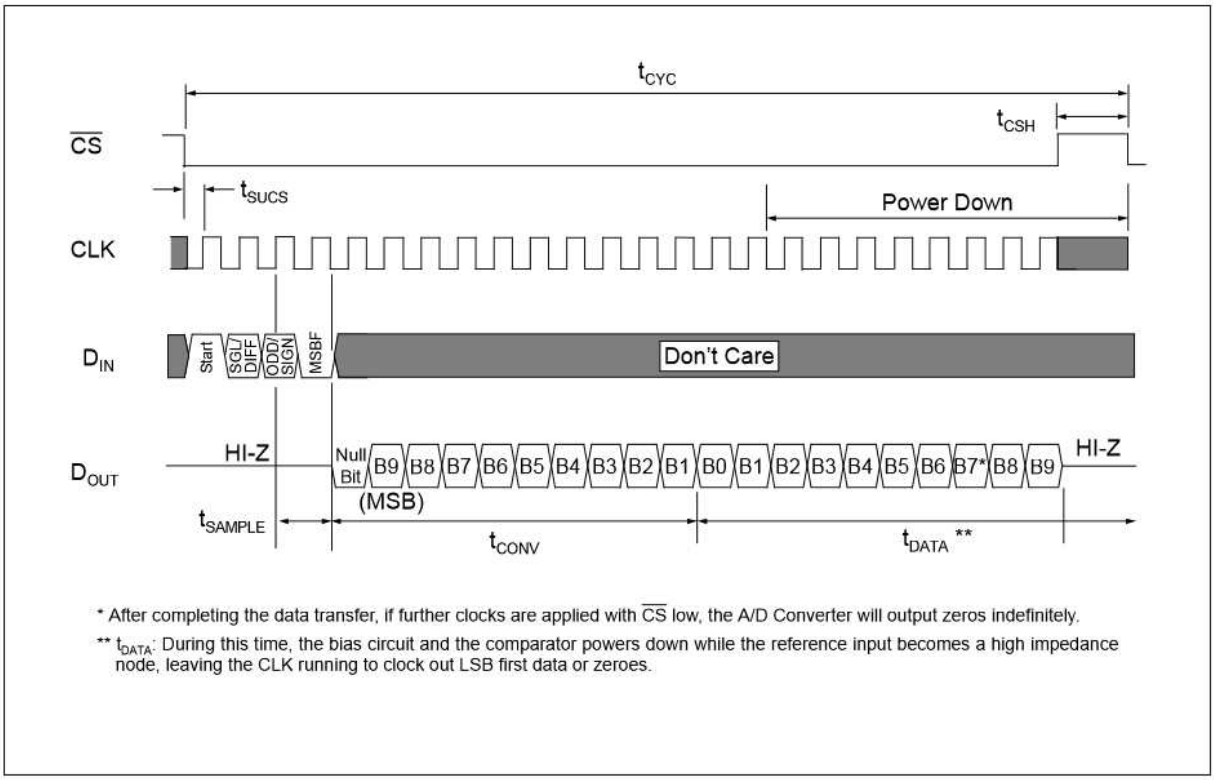


* After completing the data transfer, if further clocks are applied with $\overline{CS}$ low, the A/D Converter will output zeros indefinitely. See Figure 5-2 for details on obtaining LSB first data.

** t_{DATA}: during this time, the bias current and the comparator powers down while the reference input becomes a high impedance node.

FIGURE 5-1: *Communication with the MCP3002 using MSB first format only.*



* After completing the data transfer, if further clocks are applied with $\overline{CS}$ low, the A/D Converter will output zeros indefinitely.

** t_{DATA}: During this time, the bias circuit and the comparator powers down while the reference input becomes a high impedance node, leaving the CLK running to clock out LSB first data or zeroes.

FIGURE 5-2: *Communication with MCP3002 using LSB first format.*

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